



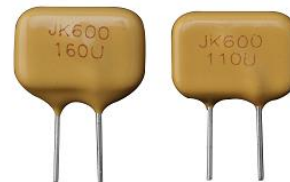
## Polymer PTC Resettable Fuse

JK600

Series

## Features:

- ✧ Radial-leaded Devices
- ✧ Cured, flame retardant epoxy polymer insulating material meets UL94V-0
- ✧ Rohs compliant and lead-free



## Product Dimensions

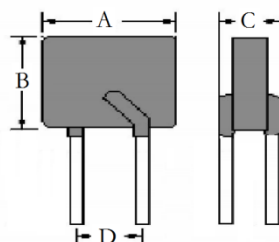


Fig1

Unit :mm

Model	Dimensions (mm)				Lead material	Shape
	A(max)	B(max)	C(max)	D(typ)	Tinned metal(mm)	Fig
JK600-110U	15	15	5.5	5.1	22AWG/Φ0.6	1
JK600-150U	15	15	5.5	5.1	22AWG/Φ0.6	1
JK600-160U	15	15	5.5	5.1	22AWG/Φ0.6	1

Note: Dimensions in the A, B, C are the maximum sizes, all typical values of D is at the tolerance of  $\pm 0.75\text{mm}$ .Thermal Derating Chart-  $I_H$  (A)

Model	Maximum ambient operating temperature ( $^{\circ}\text{C}$ )									
	-40 $^{\circ}\text{C}$	-20 $^{\circ}\text{C}$	0 $^{\circ}\text{C}$	25 $^{\circ}\text{C}$	30 $^{\circ}\text{C}$	40 $^{\circ}\text{C}$	50 $^{\circ}\text{C}$	60 $^{\circ}\text{C}$	70 $^{\circ}\text{C}$	85 $^{\circ}\text{C}$
JK600 series	147%	138%	119%	100%	92%	83%	73%	64%	55%	42%

## Electrical Characteristics

Model	$I_H$ (A)	$I_T$ (A)	$V_{MAX}$ interrupt (V)	$I_{MAX}$ (A)	$P_d$ (W)	Maximum Tim- to-Trip		Resistance( $\Omega$ )
						Current (A)	Time (S)	$R_{MIN}$ - $R_{MAX}$
JK600-110U	0.11	0.22	600	3	1.0	1.0	8	6-16
JK600-150U	0.15	0.30	600	3	1.0	1.0	9	5-14
JK600-160U	0.16	0.32	600	3	1.0	1.0	10	4-12

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**Specifications are subject to change without notice !**



$I_H$ =Hold current:Maximum current at which the device will not interrupt in 25°C still air.

$I_T$ =Trip current:Minimum current at which the device from low resistance to high resistance in 25°C still air.

$V_{MAX}$ =Maximum continuous voltage device can withstand without damage at rated current.

$I_{MAX}$ =Maximum fault current device can withstand without damage at rated voltage.

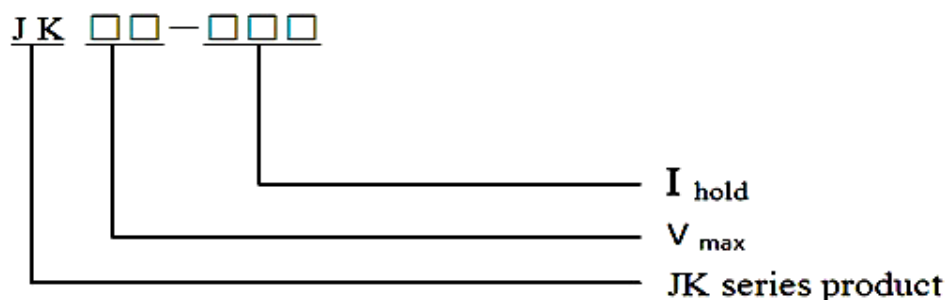
Maximum Time-to-trip:Maximum time to trip at assigned current.

$P_d$ =Typical power dissipation:Typical amount of power dissipated from the device when in 25°C still air environment.

$R_{MIN}$ =Minimum resistance of device at 25°C prior to tripping.

$R_{MAX}$ =Maximum resistance of device at 25°C prior to tripping.

### Marking System



### Environmental Specifications

Test	Conditions	Resistance change
Passive Aging	+85°C, 1000 hours	±8% typical
Humidity Aging	+85°C, 85%R.H.1000 hours	±8% typical
Thermal Shock	+125°C to -55°C, 10 Times	±12% typical
Solvent Resistance	MIL-STD-202, Method 215F	No change
Vibration	MIL-STD-202, Method 201	No change

### Soldering method

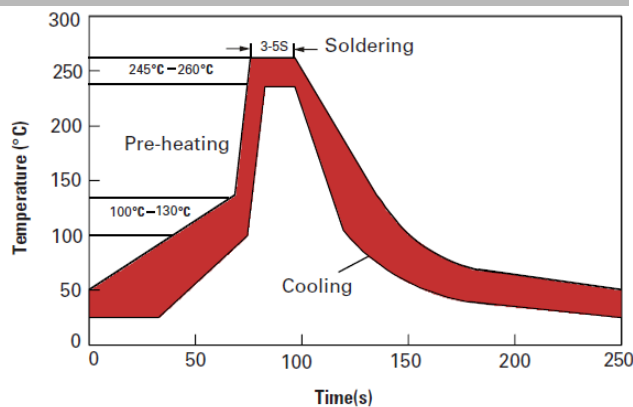
#### Wave Soldering:

Soldering Temperature:260°C~270°C

Soldering Time:≤3sec.

Soldering Position: Resettable fuse lead and the distance from the bottom ≥ 6mm

#### Manual soldering:



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Soldering Temperature: 250°C~280°C

Soldering Time: ≤3sec.

Soldering Position: Resettable fuse lead and the distance from the bottom ≥ 6mm

## Packaging and Storage

### Packaging quantity

JK600-110U~JK600-160U 200 Pcs/Bag

### Storage

The maximum ambient temperature shall not exceed 40°C. Storage temperature higher than 40°C could result in the deformation of packaging materials. The maximum relative humidity recommended for storage is 70%. High humidity with high temperature can accelerate the oxidation of the solder plating on the leads and reduce the solderability of the components. Sealed plastic bags with desiccant shall be used to reduce the oxidation of the leads and shall only be opened prior to use. The products shall not be stored in areas where harmful gases containing acid or alkali or other harmful substances are present.

### Warning:

- Please read this specification before using the product.
- Use PPTC beyond the maximum ratings or improper use may result in device damage, electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- Use PPTC with a large inductance in circuit will generate a circuit voltage above the rated voltage of the PPTC.
- Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.

Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC can be cleaned by standard methods.

### Notes:

The specification is intended to present application product and technical data to assist the user in selecting PPTC circuit production devices. However, users should independently evaluate and test the suitability of each product. Jinrui makes no warranties as to the accuracy or completeness of the information and disclaims any liability resulting from its use. Jinrui's only obligations are those in the Jinrui Standard Terms and Conditions of Sale and in no case will Jinrui be liable for any incidental, indirect, or consequential damages arising from the sale, resale, or misuse of its products. Jinrui reserves the right to change or update any information contained in this specification without notice.

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